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10/722,959

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10/24/2005 HGUTEMA2 00000002 061075 10722959

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03 FC:8001

APPLICATION NO.

FILING DATE 11/26/2003

FIRST NAMED INVENTOR

Seong-Hoon Lee

MIC-40

ATTORNEY DOCKET NO.

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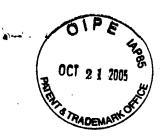
9002

TITLE OF INVENTION: DIGITAL DELAY-LOCKED LOOP CIRCUITS WITH HIERARCHICAL DELAY ADJUSTMENT

APPLN. TYPE	SMALL ENTITY	ISSUE FEE		PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400		\$300	\$1700	10/21/2005
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4a. The following fee(s) are	e enclosed: small entity discount permitte	4b. [ed) [Payment of A check Payment		(Order N closed. is attached.	0. 002322.0040
a. Applicant claims S The Director of the USPTO	s (from status indicated above SMALL ENTITY status. See is requested to apply the Issu Publication Fee (if required) words of the United States Pate	2) 37 CFR 1.27. The Fee and Publication will not be accepted to	b. Applic	cant is no longer claiming SMAI ny) or to re-apply any previously e other than the applicant; a regi	L ENTITY status. See 37 C	FR 1.27(g)(2).
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

PATENT APPLICATION

Applicant

: Seong-Hoon Lee

Application No.

: 10/722,959

Confirmation No.

: 9002

Filed

: November 26, 2003

For

: DIGITAL DELAY-LOCKED LOOP CIRCUITS

WITH HIERARCHICAL DELAY ADJUSTMENT

Group Art Unit

: 2816

Examiner

: An T. Luu

Class-Subclass

: 327-158000

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